



# GOVERNMENT POLYTECHNIC VIKRAMGAD

Electrical Engineering Department

Academic Year - 2014-15

Time Table for Fourth Semester Electrical Engg.

w.e.f.: 12/12/2014

Day \ Time	10:30-11:30 AM	11:30-12:30 PM	12:30 - 1:30 PM	1:30 - 2 PM	2 - 3 PM	3 - 4 PM	4 - 5 PM	5 - 6 PM
Monday	DMT (JP)		EME (DGP)	<b>R E C E S S</b>	E1 - IES (JPK) E2 - EST (NMK) E3 - DMT (JP)		INN (AS)	
Tuesday	IES (JPK)		INN (AS)		E1 - PPT (SVB) E2 - INN (AS) E3 - EME (DGP)		E1+E2+E3 - PPT (SVB)	
Wednesday	DMT (JP)		EME (DGP)		E3 - IES (JPK) E1 - EST (NMK) E2 - DMT (JP)		OFF	
Thursday	IES (JPK)		INN (AS)		E2 - IES (JPK) E3 - EST (NMK) E1 - DMT (JP)		OFF	
Friday	E3 - PPT (SVB) E1 - INN (AS) E2 - EME (DGP)		EST (NMK)		E2 - PPT (SVB) E3 - INN (AS) E1 - EME (DGP)		OFF	
Saturday	← TDE (BP) →				← TDE (BP) →			

**Staff:** SBV-S.B. Vishwarupe, JPK-J.P. Kshirsagar, NMK-N.M. Khadse, DGP-D.G. Pendhokare, BP-Bhavesh Patil, JP-Jagruti Patil, AS-Aswini Shelar

Academic Coordinator

Head of Electrical Engg. Dept.

Principal