



# GOVERNMENT POLYTECHNIC VIKRAMGAD

Electrical Engineering Department

Academic Year - 2014-15

Time Table for Sixth Semester Electrical Engg.

w.e.f.: 12/12/2014

Day \ Time	10:30-11:30 AM	11:30-12:30 PM	12:30 - 1:30 PM	1:30 - 2 PM	2 - 3 PM	3 - 4 PM	4 - 5 PM	5 - 6 PM
Monday	9:30 -1:30 (4 Hrs) PRO (All Batches) (SBV, KDP, JPK, NMK)			<b>R E C E S S</b>	PEL (KDP)		TME (JPK)	
Tuesday	PEL (KDP)		MAN (SVB)		E1 - PEL (KDP) E2 TME (JPK)		E2 - PEL (KDP) E1 TME (JPK)	
Wednesday	TME (JPK)		MAN (SVB)		IEN (KDP)		EIA (E1+E2)	
Thursday	IEN (KDP)		MAN (SVB)		E1 - IEN (KDP)		E2 - IEN (KDP)	
Friday	← EIA (BP) →				← EIA (BP) →			
Saturday	← E1+E2 -TME (JPK) →				← E1+E2 -TME (JPK) →			

**Staff:** SBV - S. B. Vishwarupe, KDP - Dr. K. D. Patil, JPK - J. P. Kshirsagar, NMK - N. M. Khadse, BP - Bhavesh Patil, JP - Jagruti Patil

Academic Coordinator

Head of Electrical Engg. Dept.

Principal